

What is claimed is:

1. A method, comprising:
controlling an electrical load with a first code executed by a processor;
5 releasing processor control so that the electrical load operates in an open
control mode while the first code is displaced with a second code;
and
reinstating processor control of the electrical load using the second code.
- 10 2. The method of claim 1, wherein the first code of the controlling step
is supplied from a boot read only memory (ROM).
3. The method of claim 1, wherein the controlling step comprises
loading the first code into a first memory location accessed by the processor.
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4. The method of claim 3, wherein the controlling step further
comprises loading the second code into a second memory location accessible by
the processor.
- 20 5. The method of claim 4, wherein the releasing step comprises
moving the second code from the second memory location into the first memory
location, thereby displacing the first code from the first memory location.
6. The method of claim 1, wherein the electrical load is a motor.
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7. The method of claim 6, wherein the motor supports a data storage
medium, and wherein the controlling step comprises using the motor to rotate the
data storage medium at an operational velocity and retrieving the second code from
the rotating data storage medium.
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8. A method, comprising:
using a processor to execute startup code loaded into a memory location to
initiate operational control of an electrical load;

continuing to operate the electrical load while processor operational control of the electrical load is temporarily suspended to allow replacement of the startup code with application code in the memory location; and

5 resuming operational control of the electrical load using the application code.

9. The method of claim 8, wherein the startup code of the using step is supplied from a boot read only memory (ROM).

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10. The method of claim 8, wherein the memory location of the using step is characterized as a first memory location, and wherein the using step further comprises loading the application code into a second memory location accessible by the processor.

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11. The method of claim 10, wherein the continuing step comprises moving the application code from the second memory location into the first memory location, thereby displacing the startup code from the first memory location.

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12. The method of claim 8; wherein the electrical load comprises a motor supporting a data storage medium, and wherein the using step comprises energizing the motor to rotate the data storage medium at an operational velocity and retrieving the application code from the rotating data storage medium.

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13. The method of claim 12, wherein the using step further comprises using the startup code to energize an actuator motor to bring a data transducing head into alignment with a track defined on the data storage medium, and utilizing the head to transduce the application data from said track.

14. An apparatus, comprising:
an electrical load;
a memory location; and
a programmable processor coupled to the memory location and adapted to
5 control the electrical load, wherein during an initialization process
the processor executes startup code loaded into the memory location
to initiate operational control of the load, temporarily releases
operational control of the electrical load so that the electrical load
continues to operate in an open control mode while the startup code
10 in the memory location is displaced with application code, and
resumes operational control of the electrical load using the
application code.
15. The apparatus of claim 14, further comprising a boot read only
15 memory (ROM) which stores the startup code, wherein the startup code is loaded
from the boot ROM to the memory location for execution by the processor.
16. The apparatus of claim 14, wherein the memory location of the
using step is characterized as a first memory location, and wherein the apparatus
20 further comprises a second memory location accessible by the processor and into
which the processor loads the application code.
17. The apparatus of claim 14, wherein the electrical load comprises a
motor supporting a data storage medium, and wherein the execution of the startup
25 code by the processor results in the energizing of the motor to rotate the data
storage medium at an operational velocity.
18. The apparatus of claim 17, further comprising an actuator motor
coupled to a data transducing head, and wherein the execution of the startup code
30 by the processor further results in the energizing of the actuator motor to bring the
head into alignment with a track defined on the data storage medium, the head
transducing the application data from said track.